MODEL SB-650 Frequency Display

# HEATHKIT

**ASSEMBLY MANUAL** 

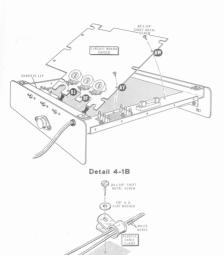




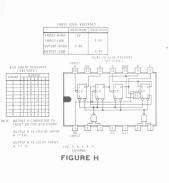
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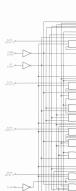


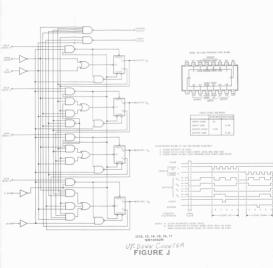
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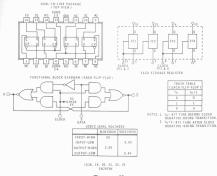


Detail 4-1C



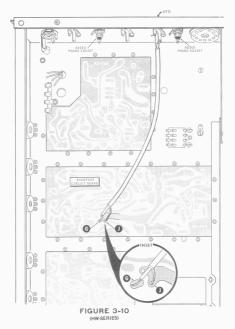


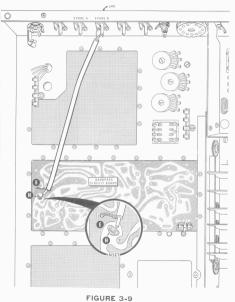




HEATHKIT

Figure K





(SB-SERIES)

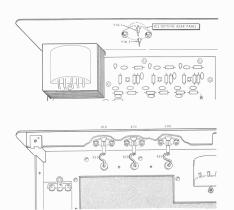
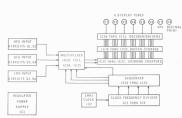
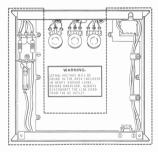


FIGURE 4-1



BLOCK DIAGRAM FIGURE 6-1



HIGH VOLTAGE AREAS

Assembly and Operation of the



## FREQUENCY DISPLAY

MODEL SB-650



HEATH COMPANY BENTON HARBOR, MICHIGAN 49022

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## INTRODUCTION

The Hearthkit Model SR-680 Frequency Display is designed to operate with Heath amateur receivers and five-band transceivers of the SR and MW series operating in the 3 to 30 MHz range. It calculates the neceived frequency to 1/10 kHz and displays the frequency on six display tubes. The only operating control is a front panel rocker switch which turns the instrument on end off.

A digital up/down counter is used to calculate the operating frequency from three oscillators — the high frequency oscillator, the linear master oscillator, and the beat frequency oscillator. However, it is not used on the 15 MHz (WWV) band of the SB-301 receiver, nor on AM signals, because outputs for all three oscillator signals are not available on the rear canels.

Because the Frequency Display is direct reading, it eliminates the problem of adding together frequencies from a switch position and two dial readings. Its superior accuracy produces the precise location of your position in the band, and its brightly illuminated display provides frequency monitoring 100% of the time.

The test equipment required for the Initial Tests and alignment of this Frequency Display is a voltmeter capable of measuring .1 volt ac and dc. A general coverage receiver may be desirable for calibration of the clock (crystal) oscillator.

Read the "Kit Builders Guide" for complete information on unpacking, parts identification, tools, wiring, soldering, and step-by-step assembly procedure.

## PARTS LIST

This "Parts List" contains all of the parts used in the assembly of the kit. Some parts, may be peckaged in envelopes with the part number of the contents printed on the contrible. Except for the initial parts sheek, retained parts in their envelopes until they are called for in the parts in their envelopes until they are called for in the seambly steps. When more than one number is on a package, disregard all but the part number listed in the "Parts List." Check each part against the following list. The key numbers correspond to the numbers on the Parts Pictorial (fold-out from Page 9).

To order a replacement part, use the Parts Order Form furnished with this kit. If a Parts Order Form is not available, refer to "Replacement Parts" inside the rear cover of the Manual.

		PARTS Per Kit		RICE		PART No.	PARTS Per Kit	DESCRIPTION	PRICE Each
RES	SISTORS				Pre	sision Res	istors, 1%		
					A2	2-239	1	1900 Ω	.25
	E: The folk rwise indicate		tors are 10%, 1/2-watt, un	ess	A2	2-33	1	6838 Ω	.25
					CO	NTROL			
A1	1-62	1	51 Ω, 5% (green-brown-	.10					
			black-gold)		A3	10-52	3	2000 Ω (2 k)	.35
A1	1-3	6	100 Ω (brown-black-brown)	.10					
A1	1-66	1	150 Ω (brown-green-brown)	.10	CA	ACITOR	S		
A1	1-137	1	200 Ω, 5% (red-black-	.10	l				
			brown-gold)		Dist				
A1	1-42	3	270 Ω (red-violet-brown)	.10	A4	21-32	6	47 pF	.10
A1	1-4	1	330 Ω (orange-orange-	.10	A4	21-22	1	220 pF	.10
			brown)		A4	21-140	11	.001 μF	.10
A1	1-7	2	680 Ω (blue-gray-brown)	.10	А4	21-71	4	.001 µF (1.4 KV)	.10
A1	1-9	3	1000 Ω (brown-black-red)	.10	A4	21-36	1	.002 µF	.10
A1	1-20	6	10 kΩ (brown-black-orange		A4	21-143	1	.05 μF	.20
A1	1-24	1	33 kΩ (orange-orange-	.10	A4	21-95	5	.1 µF	.15
			orange)						
A1	1-25	1	47 kΩ (yellow-violet-	.10	Oth	er			
			orange)		A5	25-248	1	100 µF electrolytic	.50
A1	1-26	9	100 kΩ (brown-black-	.10	A6	25-230	2	2000 μF electrolytic	1.90
A1	1-27		yellow)	.10	A7	27-71	1	1 µF Mylar*	1.30
A1	1-27	1	150 kΩ (brown-green- vellow)	.10	A8	31-57	1	Trimmer, 2.7-20 pF	.65
A1	1-29	3	yeriow) 220 kΩ (red-red-	.10					
0.1	1.60	- 4	SEC USE LIAM LOD.	0					

DuPont Registered Trademark

KEY No.	PART No.	PART Per Ki		PRICE Each		PART No.	PARTS Per Kit	DESCRIPTION	PRICE
DIC	DES-T	RANSIST	ORS*		на	RDWAR			
A9	56-57	1	1N716A, 12 V zener	.65	#4	Hardware			
			diode (violet-brown-blu	0-	D1	250-285	2	4-40 x 1/4" screw	.05
			brown)		D2	250-4	8	4-40 x 3/8" screw	.05
A10	57-27	1	1N2071 diode	.50	D3	250-34	9	4-40 x 1/2" screw	.05
A10	57-65	4	1N4002 diode	.20	D4	252-2	17	440 nut	06
A11	417-18	54 3	2N2369 transistor	1.65	D5	254-9	17	#4 lockwasher	.05
A12	417-24	10 2	40673 transistor	2.40					
A12	417-27	74 1	40673 transistor	2.25	#6	Hardware			
INT	EGRA	TED CIRC	:UITS*	- 4	E1	250-416	2	6-32 x 1/4" flat- head screw	.05
NOT	E. The	profix and	suffix letters may vary b	or the	E2	250-170	14	#6 x 1/4" sheet metal	.05
			to those listed below.					SCIENT WINDLINGS	.05
1101111	~131103	ii correspon	2 to trioge rigited below.		F3	250-89	5	6-37 x 3/8" screw	.05
B1	442-30	1	UA309K 5V regulator	4.65	E4	250-206	4	6-32 × 11/16" screw	.05
B2	443-1	. 6	SN7400N guad NAND	45	E5	250-237	2	#5 x 3/8" sheet metal	.05
			gate					5278W	.00
B2	443-5	1	SN7473N dual J-K	5.20	E6	250-305	2	6-32 × 9/16" stud	.05
			flip-flop		E7	250-40	7	8-32 x 1-1/2" screw	.05
82	443-7	6	SN7490N DCU	1.95	EB.	252-3	- 11	6-32 nut	.05
B3	443-13	6	SN7475N quad latch	1.95	E9	252-22	4	5-37 Speed NurtA	.05
B3	443-35		SN7441B or MC7441Al decoder/driver		F1	254-1	15	#6 internal tooth	.05
вз	443-66	6	SN74192N Up/down	5.00	F2	254-6	11	#5 external tooth	.05
			counter SN74H103N J-K	1.45				lockwasher	
B2	443-70	1		1,45	Oth				
		_	flip-flop SN74H00N NAND gate	70	F3	204-9			
B2	443-71	2	SN/4HOUN NAME gate	-70	Fd	252-10	8	Angle bracket Push-on nut	.10
					F5	253-8	6	7/16" OD brees	.05
			integrated circuits are man	ked for	20	200	0	Windows OF DESIGN	.05
ident	ification	n in one of t	he following four ways:	100000000000000000000000000000000000000	F6	253-84		3/R" OD flat	
	1. F	art rumber		5 1	100	200-64			.05
			integrated circuit type numb	_=====================================	F7	255-2	9	washer Spacer	
			and type number.	* -21	352	2007		Spacer	.05
			with type number other th	on the	con	KETS-PL	He		
		ne listed.	with type number outer to	7.12	300	KEISEL	.00		
		ine listed.			FR	434-42	3		BES SA
CLIE	ET ME	TAL PAR	TS	5 12 1	F9	434-201	6	Phono socket Tube socket	1.10
SHE	E I WE	TALFAR	13			434-201	16	14-pin IC socket	.20
	90-552	-1 1	Cabinet	6.70		434-225	18	16-pin IC socket	20
	200-62		Chassis	1.55		438-4	6		
C1	200-62		Front panel	1.15		434-107	3	Phono plug Phono socket,	.10
C2 C3	203-88		Rear panel	.70	-13	434-107	3	bushing mount**	.40
C3 C4	203-88		Circuit board shield	1.80				busning mount"	
C4 C5	205-87		Light shield	.50	40				
			Top shield	.80	-Meg	istered Tra	oemark Tin	nerman Company	
C6	206-55		Top shield Rottom shield	.80	***				
C7	206-55	6 1	Bottom shield	.60	ro	r modificat	ion or recen	ring equipment.	

Ī.	X-1100	ATHICIT	1					F	age 7
KEY No.	PART No.	PARTS Per Kit	DESCRIPTION	PRICE Each		PART No.	PARTS Per Kit	DESCRIPTION	PRICE Each
WIE	E AND C	ABLE			Misc	ellaneous	(cont'd.)		
						391-34	1	Blue and white label	
	89-23	1	Line cord	.75	H2	391-78	1	Nameplate	1.35
	340-3	1	Bare wire	.05/ft	H3	404-424	1	Crystal	5.70
	343-2	1	RG-58A/U coaxial	.10/ft	H4	411-264	6	Display tube (NL1220)	7.15
			cable (large)	1	H5	412-11	1	NE-2 neon lamp (small)	.20
	343-12	1	RG-174 coaxial cable (small)	.10/ft	.H6	412-49	1	NE-51H neon lamp (large)	.40
	344-46	1	White wire	.10/ft	H7	421-13	1	1/2-ampere 3 AG fuse	.15
	344-52	1	Red wire	.05/ft	HB	422-1	1	Fuse block	.25
	344-56	1	Blue wire	.06/ft	H9	431-10	2	3-lug terminal strip	.10
				-	H10	446-95	1	Window	1.70
PLA	STIC FIE	BER AND	RUBBER PARTS		H11	475-10	3	Ferrite bead	.10
					H12	490-5	1	Nut starter	.10
G1	73-3	6	1/2" grommet	.10		490-111	1	Integrated circuit puller	.10
G2	73-59	3	1/4" grommet	.10			1	Manual (See front cover	2.00
G3	75-52	1	Insulator	.10				for part number.)	
G4	75-71	1	Strain relief	.10		597-260	1	Parts Order Form	
G5	207-4	2	Cable clamp	.10		597,308	1	Kit Builders Guide	
G6	255-59	2	Tapered spacer	.10				Solder (Additional 3'	
G7.	261-9	4	Rubber foot	.05				rolls of solder, #331-6.	
								can be ordered for 15	
MIS	CELLAN	EOUS						cents each.)	
G8	45-42	2	RF chake	.20					
G8	45-76	1	RF choke	.20					
	54-298	1	Power transformer	5.00				ly on purchases from the	
G9	60-48	1	DPDT rocker switch	.55				is to a U.S.A. destination	
	74-6	1	Masking tape	.25				to the price when ordering	
G10	75-108	2	Fish paper	.10				to cover local sales tax, p	
			2" x 1-3/4"					e U.S.A. parts and servi	
	85-1445-1	1	Circuit board	9.55				Heathkit source and will	
H1	205-778	1	1" steel blade	.10	add?	tional tra	nsportation,	taxes, duties, and rat	es of
	390-362	1	Fuse label	.10	exch	ange.			

Before starting to assemble this kit, read the "Kit Builders Guide" for complete information on wiring, soldering, and step-by-step assembly propedures.

Resistors are designated by the color code and the resistance value. The symbol  $\Omega$  means ohms, and K or  $k\Omega$  indicates 1,000 ohms. Capacitors are designated by their value and type. The symbol  $\mu$ F means microferard, and  $\mu$ F means picofarad,  $\mu$ F is eauls to 1,000,000  $\mu$ F.

At this time, lay aside the four .001 µF (1.4 kV) disc capacitors so they will not be confused with others. These capacitors will be used when the primary circuit of the DOWN transformer is wired.

Due to the small foll area smooth the circuit board holes and the until a real services folls, it is necessary to use the until area set between folls, it is necessary to use the untroit care to prevent solder bridge between adjacent foll area. Det a minimum amount of solders and do not hast area. Det a minimum amount of solders and do not have the care of the contract of the con



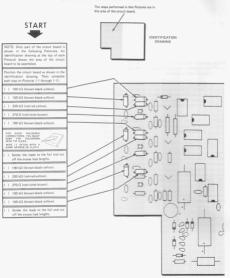
Figure 1-1

NOTE: If a small wattage, small-tip soldering iron is not smallbable, proceed a follow: Be sure your soldering iron is cool. Then wrap the bare wire, supplied with this kit, tightly consult the soldering iron tip as shown in Figure 11. Allow approximately 1/4" of wire to extend beyond the end of the approximately 1.4" of wire to extend beyond the end of the support of the suppor

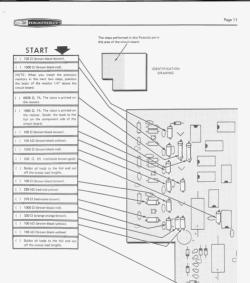
Most parts will be installed on the top (the side with the component outlines) of the circuit board, and the leads will be soldered to the foil (other) side. Solder the leads only to the foil side of the board unless specifically instructed to do otherwise. Due to the nature of the board, solder will be drawn through the circuit board plated-through holes to the fool (one) one side of the circuit board plated-through holes to the (component side) of the circuit board. This is normal.



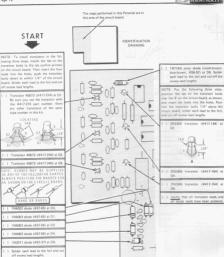
#### CIRCUIT BOARD ASSEMBLY



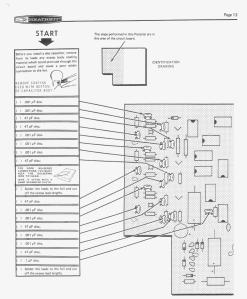
PICTORIAL 1-1



PICTORIAL 1-2



PICTORIAL 1-3



PICTORIAL 1-4





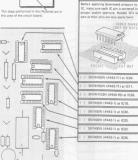
Be very careful when you install the sockets, as it is possible to erroneously place a 14-pin socket in a 16-pin socket each socket to the similarly-shaped mark



( ) Install 14-pin dual-in-line sockets at IC10 11 24 25 32 33 34 and 35 NOTE: An IC Lifter has been furnished to

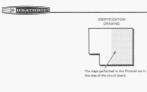


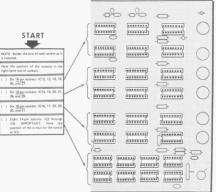
the longer portion back and forth. Be very



PICTORIAL 1-5

IDENTIFICATION





PICTORIAL 1-6

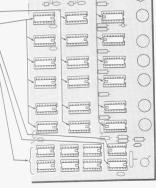
START

IC type number will vary with the manufacturer, but the numbers must correspond. Be sure, however, to install the proper IC number at each location.



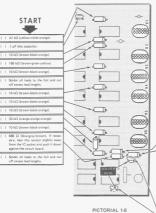
The steps performed in this Pictorial are in this area of the circuit board.











## CONTINUE

 Remove the two pins shown from each of six tube sockets, Grasp each pin from the end with long-nose piters. Then pull the pins straight out.

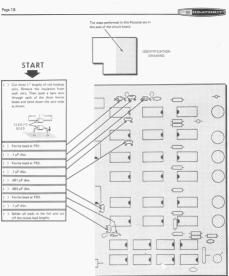


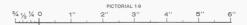
- Install tube sockets at V1, V2, V3, V4, V6, and V6. Solder each pin to the foil. Take care not to form any solder bridges between foils.
- ( ) 680 Ω (blue-gray-brown)
- ( ) 150 Ω (brown-green-brown).
- ( ) Solder all leads to the foil and cut off the excess lead lengths.

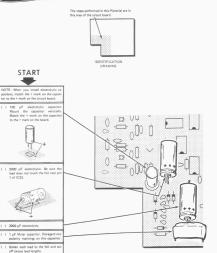
NOTE: As you imstall each of the following capacitors, solder the leads to the following capacitors, solder the leads to the following the mid-capacitor on the component side of the circuit board. Solder the other lead to the

( ) .001 uF disc.

( ) .05 μF disc.







PICTORIAL 1-10



START

NOTE: In the following three steps, insert each wire into its lettered hole from the foil side of the circuit board. Solder each wire to the foil. Cut off excess lead lengths on only the top of the circuit board.

- ( ) Cut three 2-1/2" lengths of red hookup wire. Remove 1/4" of insulation from both ends of each wire.
- ( ) 2-1/2" red hookup wire at hole F.
- 1 2-1/2 red hookup with at hose
- Position the crystal (#404-424)
  - ) Position the crystal (#404-424) 1/4" above the circuit board. Solder the leads to the foil and cut off the excess lead lengths.

CONTINUE

## $\bigcirc$

Install the 2.7-20 pF trimmer.



( ) Turn the top plate of the triemer cases/for so it covers half the lower

plate

LOWER

NOTE: Disregard any unmarked unused

Inspect the circuit board carefully for solder bridges. Be sure that all transistors, IC's, and diodes are installed correctly and at the proper locations. A magnifying glass will be helpful. Then lay the circuit board saide; it will be installed later.

FINISH

#### CHASSIS ASSEMBLY

The illustrations in this section of the Manual are called Pictorials and Details. Pictorials show the overall operation for a group of assembly steps; Details are used in addition to the Pictorials to illustrate a single step. When you are directed to refer to a certain Pictorial "for the following steps," continue using that Pictorial until you are referred to another Pictorial for another aroup of steps.

Look at the "Chassis Photos" (Page 81) from time to time to see the actual positions of wires and components.

Lockwahers and nuts will be used with most screw when mounting parts. Consequently, the applicable steps will call out only the size and type of hardware used. For example, the phrase "Like 623 x 1/4" screws, can go and 632 nuts. 1/4" screws, one or more #8 lockwahers, and 632 nuts. Fefer to the Details for the proper installation of hardware. Be sure to position the parts as shown in the Pictorials. Read the entire step before performing the operation and follow

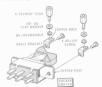
When a step directs you to "connect" an insulated wire, first prepare its ends by prepaying 1/4" of insulation.

A plastic nut starter has been provided with this kit. Use it to hold and start 6-32 and 4-40 nuts on screws. Refer to the "Kit Builders Guide" for more information.

#### CHASSIS PARTS MOUNTING

Refer to Pictorial 2-1 (fold-out from Page 23) for the following steps,

- Refer to Detail 2-1A and mount two angle brackets on the rocker switch. Use 4-40 x 1/4" screws in the tapped holes of the switch mounting ears. Position the angle brackets as though
- Refer to Detail 2-1A and install a 6-32 x 9/16" stud in the tapped hole of each angle bracket. Use a 3/8" OD flat washer and a #6 internal tooth lockwasher on each stud.



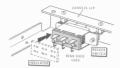
Detail 2-1A



Detail 2-1B

- ( ) Refer to Detail 2-18 and push the switch rocker into the rectangular hole in the front of the chasis at A. Then sight down through holes AB and AC to make sure that the tapped holes in the two studs are centered under holes AB and AC. If not, loosen the hardware and realion the anole brackets as necessary.
- ( ) Secure the two studs to the chassis lip at AB and AC.





Detail 2-10

- Refer to Detail 2-1C and push the insulator (#75-52) down over the six solder lugs of the rocker switch. Then bend the row of three switch lugs farthest from the chassis lip over against the insulator.
- Inspect the rocker to make sure it does not rub on the edges of the opening as it is operated. If it does, bend the angle brackets as necessary to provide clearance.



Detail 2-1D

- Refer to Detail 2-1D and install 1/4" grommets in chassis holes BE, BG, and BK.



Detail 2-1E

 Refer to Detail 2-1E and install Speed Nuts on the chassis lips at holes AA, AD, AE, and AF. Be sure the curved lip of each Speed Nut is on the inner side of the chassis lio.



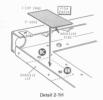
Detail 2-1F

- ( ) Refer to Detail 2-1F and mount the fuse block on the chassis at B.A. Use 5-32 x 3/8" hardware
- ( ) Push the fuse into the fuse block.
- Remove the protective covering from the fuse label and press it into place in the location shown in the Pictorial.



Detail 2-1G

- Refer to Detail 2-1G and cut a 2" x 1-3/4" fish paper so it is 2" x 1-1/4".
- Remove and discard the white protective covering from the fish paper.



 Refer to Detail 2-1H and place the adhesive side of the fish paper against the chassis so it covers hole BB, one of its 1-1/4" edges just touches the edge of hole BC, and the 2" edge is snua against the chassis lio.

- With a pointed instrument, make a hole through the fish paper at BB.
- NOTE: In the following two steps, be sure you position the terminal strip mounting feet as shown in the Pictorial.
- Refer to Detail 2-1J and mount a 3-lug terminal strip at BB. Use 6-32 x 3/8" hardware. Start the nut on the screw with the plastic nut starter.



( ) Similarly, mount a 3-lug terminal strip at BC. Note the

#### CIRCUIT BOARD MOUNTING

Refer to Pictorial 2-2 (fold-out from this page) for the following steps.

- ( ) From the bottom of the chassis, insert 4-40 x 1/2" screws into holes AK, AM, AR, AU, AX, AZ, BD, BH, and BL. Hold each screw in place with an inch of masking tape over the screw head (holes BD and BH are the two holes closest to the rear edge of the chassis).
- Position the chassis so the screw threads are up; then
  place a spacer and a #6 external tooth lockwasher on
  each screw.
- Hold the circuit board above the chassis and start a red wire into each of the chassis grommets. Then gently lower the circuit board so a screw enters each circuit board mounting hole.
- ( ) Place a #4 internal tooth lockwasher on each screw,
- ( ) Start a 4-40 nut on each screw. Then remove the tape from the screw heads and tighten the hardware. Do not overtighten; the circuit board can be damaged. NOTE: The foil at hole AR is a ground foil and it is all right for the lockweather to touch it.



#### PANEL PREPARATION AND MOUNTING

#### Refer to Pictorial 2-3 for the following steps.

NOTE: When you apply the push-on nuts in the following step, be very careful and do not break the plastic studs on the back of the window. Twisting the nut on the stud may break the stud. Therefore, position the nuts exactly as shown in the Pictorial and push them straight down onto the studs. A nut driver may be used in applying the "Dush."

#### Refer to Detail 2-3A for the following three steps.

- Place the plastic window on a soft cloth on your work table with the four studs pointing up and the "kHz" marking to your left.
- Place the front panel (#203-882-1) on the four window studs with the rectangular opening at A, to your right. It is important that you have these two parts properly oriented. The window, once installed, cannot be remounted.
- ( ) Hold a push-on nut with its longer edge parallel with the longer edge of the panel, as shown, and push it straight down onto one of the four window studs. Push straight down until the nut is snug against the panel.
- In the same manner, install push-on nuts on the other three window studs. Position the nuts as shown.
- Refer to Pictorial 2-3 and mount the front panel on the chassis. Use #6 internal tooth lockwashers and 6-32 nuts at holes AN and AT. Do not tighten the nuts.

- ( ) Place the two studs of the nameplate in holes AP and AS (make sure the plate is positioned as shown). Secure them with push-on nuts. NOTE: If you have any difficulty, position the panel face down and place the edge of a magazine under the nameplate ONLY to support it while you apply the push-on nuts.
- Make sure the bottom of the front panel is aligned with the bottom of the chassis and tighten the nuts at AN and AT.
- Remove two 1/4" lengths of insulation from the white wire,
- Slide the 1/4" lengths of insulation onto each lead of the 5-volt regulator IC (#442-30).



 Refer to Detail 2-3B and mount the IC at BR and BS on the outside of the rear panel. Use 6-32 x 3/8" hardware.

### GROUND



- ( ) Prepare three 1-1/2" lengths of blue wire.
- Refer to Detail 2-3C and solder a 1-1/2" blue wire to the ground (shorter) lug of each of three phono sockets.
   Refer to Pictorial 2-3 and mount the rear panel on the
- chassis with 4-40 x 3/8" hardware at BN and BP. Tighten the hardware only finger tight.
- ( ) Mount the three phono sockets at holes C, D, and E with 4-40 x 3/8" hardware. Position the socket lugs as shown. Hold the sockets centered in their holes by plugging a phono plug in each socket until the hardware is tightened.
- Tighten the hardware at holes BP and BN. Then remove the phono plugs.



Detail 2-3D

#### Refer to Detail 2-3D for the following two steps.

- Remove the two paper bands from only the bottom of the power transformer if your transformer was furnished with these strips.
- ( ) Remove the two 6-32 nuts from the power transformer. Handle the transformer carefully so you do not loosen any of the core laminations and be careful not to break the ground wire when you remove the nuts.
- ( ) Mount the transformer on the top of the chassis at AG and AH. Be sure to position the leads toward hole AH. On each mounting screw, use a #6 internal tooth lockwasher and one of the 6-32 nuts just removed.

#### CHASSIS BOTTOM WIRING

Refer to Pictorial 2-4 (fold-out from Page 27) for the following steps.

NOTE: In the following steps, use the four .001  $\mu{\rm F}$  (1.4 kV) disc capacitors set aside earlier.

- Refer to the inset drawing on the Pictorial and connect a .001 µF disc capacitor between lugs 1 (NS) and 2 (NS) of terminal strip BC.
- Connect a .001 μF disc capacitor between lugs 2 (NS) and 3 (NS) of terminal strip BC.
   Connect a .001 μF disc capacitor between lugs 1 (NS)

and 2 (NS) of terminal strip BB.

 Connect a .001 μF disc capacitor between lugs 2 (S-2) and 3 (NS) of terminal strip BB.





Detail 2-4A

- ( ) Cut the leads of the two RF chokes (#45-42) to 5/8".
- ( ) Connect an RF choke (#45-42) from lug 1 of terminal strip BB (NS) to lug 1 of terminal strip BC (NS). Wrap the leads around the terminals on the side away from lug 2.
- Connect an RF choke from lug 3 of terminal strip BB (NS) to lug 3 of terminal strip BC (NS). Wrap the leads around the terminal on the side away from lug 2.

NOTE: As you connect each of the next four wires, wrap the wire ends around the lugs as shown in the Pictorial.

- Prepare a 1-1/2" white wire. Connect this wire from lug 3 of terminal strip BB (S-3) to lug 1 of the fuse block (S-1).
- Prepare three white wires of the following lengths.
   Twist the ends of each wire tightly together and melt a small amount of solder on each end.

NOTE: Route the following wires as shown in the Pictorial.

 Connect the 22" white wire from lug 1 of terminal strip BB (S-3) to lug 4 of the power transformer (NS).

- ( ) Connect the 11" white wire from lug 2 of the fuse block BA (S-1) to lug 3 of the switch (S-1),
- Connect the 12-3/4" white wire from lug 2 of the switch (S-1) to lug 1 of the power transformer (NS).

Refer to Detail 2-4A for the following steps:

- ( ) Separate the line cord conductors for a distance of 1-1/2" and, if not already done, remove 1/4" of insulation from the end of each wire. At the end of each conductor, twist the fine strands of wire tightly together and melt a small amount of solder on each end.
- ( ) Place the strain relief on the line cord 2" from the end. Then insert the free end of the line cord into hole B from the outside of the rear panel, with the beaded wire positioned as shown. Use pilers to compress the strain relief and insert is into hole.

Refer to Detail 2-4B and connect the line cord conductors as follows:

- Connect the green wire to lug 2 of terminal strip BC. Wrap the bare end back over the top of the solder lug (5-3).
- Connect the line cord conductor with the smooth edge to lug 3 of terminal strip BC. Wrap the bare end of the wire around the edge of the lug away from lug 2 (S-3).
- Connect the line cord conductor with the beaded edge to lug 1 of terminal strip BC. Wrap the bare end of the wire around the edge of the lug away from lug 2 (S-3).

#### TRANSFORMER PRIMARY WIRING

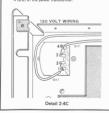
This Frequency Display can be wired to operate from either 120 VAC or 240 VAC. 50/60 Hz. line voltage. Therefore. two sets of steps are given for the wiring of the power transformer. Follow only the set of steps that agrees with the line voltage in your area.

All wires connected to lugs 1, 2, 3, and 4 of the power transformer must be wrapped around the sides of the solder lugs so the clearance to the bottom shield (to be installed later) will not be reduced.

In the following steps, prepare the required small bare wires by removing the insulation from 7/8" lengths of red wire

#### 120 VAC Wiring

- ( ) Connect a 7/8" small bare wire from lug 1 (S-2) to lug 3 (S-1) of the power transformer
- ( ) Connect a 7/8" small bare wire from lug 2 (S-1) to lug 4 (S-2) of the power transformer



#### 240 VAC Wiring

- ( ) Connect a 7/8" small bare wire from lug 3 (S-1) to lug 2 (S-1) of the power transformer.
- ( ) Solder the white wires at lugs 1 and 4 of the power transformer.



NOTE: The blue and white label shows the model number of your kit. Refer to these numbers in any communications with the Heath Company.

( ) Refer to Detail 2-4E and install the blue and white identification label. Position the label within the outline on the rear panel so it exposes the correct wiring information and covers the incorrect information. Carefully peel away the backing paper. Then press the label into position. You will avoid smearing the numbers on the label if you will put the piece of waxed backing paper on top of the label and then rub on it instead of directly on the label.





Detail 2-5A

#### CHASSIS TOP WIRING

- ( ) Prepare the following wires:
  - 8" red

#### Refer to Pictorial 2-5 for the following steps,

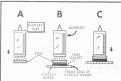
hole I in the circuit board (S.1).

hole K in the circuit board (S-1),

- ( ) Connect the 7" blue wire from lug 1 of IC1 (S-1) to hole M on the circuit board (S-1) ( ) Connect the B" red wire from Jun 2 of IC1 (NS) to
- hole I in the circuit board (S-1). Use the indicated hole at location L. ( ) Connect the 10" red wire from lug 2 of IC1 (NS) to
- ( ) Connect the 11" red wire from lug 2 of IC1 (S-3) to
- Connect the indicated leads of the power transformer to the

circuit board as follows:

- ( ) Either blue to hole A (S-1).
- ( ) Blue-vellow to hole B (S-1).
- ( ) Other blue to hole C (S-1)
- ( ) Either red to hole D (S-1).
- ( ) Other red to hole E (S-1).
- ( ) Cut off any excess lead lengths.



- - PART 8: WITH THE NUMBERS IN THE DISPLAY TURE FACING
  - PART C. MAKE SURE THE TUBE LEADS MATCH THE TUBE SOCKET

#### Detail 2.58

- ( ) Refer to Detail 2-5A and use diagonal cutters to cut off the two pins indicated on each of the six display tubes. Cut off the pins on the lower side of the tube hase.
- ( ) Refer to Detail 2-5B and install the six display tubes on the circuit board at V1 through V6. Be sure the numbers inside the tubes are facing toward the window in the front panel.
- ( ) Cut a 1/2" length of red hookup wire. Remove the insulation in one piece and slide it onto one lead of an NE-51H neon lamp.
- ( ) Refer to Detail 2-5C and install the NE-51H (large) neon lamp at V7 with the insulated lead in the hole farthest from the edge of the circuit board. Position the lamp base 1/4" above the circuit board. Solder the leads to the foil and cut off excess lead lengths.
- ( ) Install the NE-2 neon lamp at VB. Position the lamp base 1/4" above the circuit board. Solder the leads to the foil and cut off excess lead lengths.

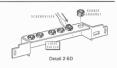




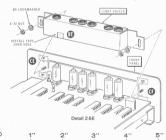


Install the two pieces of masking tape on the back of the light shield as follows:

- ( ) One piece of tape over hole BT as shown in Detail 2-5E.
- ( ) One piece of tape over rectangular hole BU as shown in the Pictorial 2-5.



- Refer to Detail 2-5E and install the light shield on stude CE and CF on the back of the front panel. Use a #6 internal tooth lockwasher and a 6-32 nut on each stud. Be sure the pointed end of a display tube enters each grownest.
- Position lamp V7 behind opening BU in the light shield.
- Position lamp V8 behind hole BT in the back of the light shield. NOTE: The lamp will shine through this small hole to provide a decimal point for the display.
- Inspect the leads of V7 and V8 to make sure none of the leads are touching each other.



#### CABLE PREPARATION

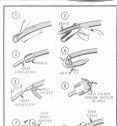
Three cables will connect the Frequency Display to your receiver during the "finitial Tests" and later during normal operation. It is important that you carefully prepare the cables. Do not use excessive heat; it can damage the inner insulation of the coaxial cable.

Flat brass washers are used in the cable assembly to provide a convenient means of grasping the phono plugs when they are removed from their sockets.

 Cut the length of RG-58A/U (large) coaxial cable into three equal lengths.

Refer to Pictorial 2-6 and prepare both ends of one of the cable lengths as follows. The circled numbers refer to the numbered parts of the Pictorial.

- ( ) ①② Remove 1" of outer insulation from both ends of a coaxial cable. Be careful not to nick the braid with your knife.
- ( ) (3) Push the shield braid back until it loosens and bulges out.
- ( ) (4) Make a hole in the shield braid, bend the cable sharply, and pull the inner insulation (and center conductor) out of the shield braid.
- ( ) (a) Pull the shield braid out into a tab at right angles to the cable. Use a minimum amount of heat and melt a small amount of solder onto the shield braid close to the inner insulation. Then remove 3/4" of inner insulation from the center conductor.
- ( ) (6) Melt a small amount of solder around the outside of the opening of the phono plug.
- ( ) Thold the tip of the phono plug in a vise (or pliers with a rubber band around the handles) and place a 716" OD brass washer around the phono plug opening. Heat the washer with a soldering iron until the solder melts and the washer adheres to the phono plug.



#### PICTORIAL 2-6

SOLDER AND

- ( ) (3) Insert the coaxiel cable into the phono plug, Solder the shield braid to the brass wather and the center conductor to the tip of the phono plug. Then cut off the excess lengths of shield braid and center conductor. File the cut end of the braid until it is smooth to the touch.
- Use an ohmmeter to make sure there is no short circuit between the center conductor and the cable shield braid.
- In like manner, install phono plugs and brass washers on both ends of the remaining two cables.

# INSTALLATION

The installation of the Frequency Display requires that your equipment (receive or transcrived furnishes three signats). These are the outputs of the IFFO flight frequency conditator), LMO/FO (Illness matter collitator), and IFFO (Illness frequency conditator), and IFFO (Illness frequency conditator), and IFFO (Illness frequency conditator). This section of the manual presents the steps necessary to provide these three signals from Heath Schard transcrivers of the SB and HIV (ISP) 4000, HIV-100, cell, series and from amateur receivers of the SB areis. No instructions for other makes of equipment are available,

Three unused phono sockets must be available on the rare panel of your receiving equipment. If they are not, they must be installed. Your kit contains three phono sockets (with mounting hardware) for this purpose. Figure 3-1 is the socket mounting detail, DO NOT drill any holes until you have carefully read the "Installation" instructions for your equipment.

Before deciding upon the location for the mounting holes, be sure no cabinet lips, interior mounted parts, or other interference will exist. Select the socket locations to retain rear panel symmetry. Use a center punch to mark the centers of the 1/4" holes to be drilled.



Figures 3-2 and 3-3 show two examples of where to locate additional phono sockets. Figure 3-2 shows an SB-102 transceiver. The original two spare phono sockets (spare A and spare B) are used. The ground terminal was moved and its original mounting hole was drilled out to provide a location for the third phono socket. Figure 3-3 shows where three phono sockets were added to an SB-301 receiver.

Install the required number of phono sockets on the rear panel of your receiver or transceiver. Then select the appropriate following section and perform the indicated staps to connect the three required signals to the sockets. Uson combletion, proceed to "inhital Tests" on Pace 38.

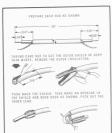


Figure 3-2



Figure 3-3

### TRANSCEIVERS



REMOVE THE INNER INSULATION AND STRETCH OUT THE SHIELD, APPLY A SMALL AMOUNT OF SOLDER TO THE END OF THE SHIELD AND THE INNER LEAD, USE ONLY ENOUGH HEAT FOR THE SOLDER TO FLOW.



Figure 3-4

## BFO MODIFICATION (SB and HW Series)

Refer to Figure 3-4 for the following steps.

- Cut a 20" length of RG-174 coaxial cable and remove 3/4" outer insulation from each end.
- ( ) At each end of the coaxial cable, pull the inner insulation and center conductor from the outer shield.





### Figure 3-5

Figure 3-6

- Remove 1/4" of insulation from the center conductor and trim off any stray strands of wire from the shield braid.
- Refer to Figure 3-5 and cut the leads of a 220 pF capacitor to 1/4". Form the leads as shown.
- ( ) Refer to Figure 3-6 and connect one end of the cable center conductor to the 220 pF capacitor (S-1).

### Refer to Figure 3-7 for the following steps:

- ( ) With the under side of the chassis positioned as shown, push the free end of the coasial cable under the switch shaft and through grommet CB until about 3" remains over the modulator circuit board.
- Connect the foot of the 220 pF capacitor to the foil at point A (S-1).
- ( ) Connect the shield braid to the foil at Point B (S-1).
- Pull any excess cable through grommet CB, but do not place any strain on the capacitor or braid connections.
- Position the coaxial cable as shown and connect the center conductor at the free end to the center lug of an unused phono socket (S-1). Connect the braid to the ground lug of the same socket (S-1).
- ( ) On the outside of the rear panel, mark this socket "BFO."

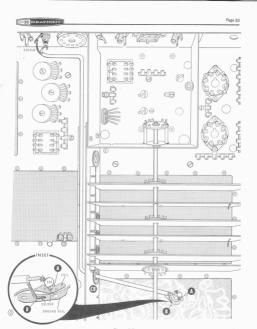


Figure 3-7

#### HFO MODIFICATIONS (SB and HW Series)

Refer to Figure 3-8 for the following steps (HW-#series illustrated).

- ( ) Use a knife with a sharp point to break the foil near pin 8 of V19. A narrow strip of foil 1/32" to 1/16"
- wide should be cut and removed. ( ) Cut the leads of a 51  $\Omega$ , 5% resistor (green, brown, black, gold) resistor to 3/8" and form them as shown in the inset drawing.
- Solder the foot of one resistor lead to the foil at point C. Solder the other foot to the ground foil at point D.
- Cut a length of RG-174 coaxial cable (small) long enough to reach from point C in the preceding step to an unused phono socket, plus 1" (so the connections will not be under stress).
- Remove 3/4" of outer insulation from each end of the cable, pull the inner conductor out of the shield as before and remove 1/4" of insulation from the ends of the center conductor.
- At one end of the cable, connect the center conductor to point C (S-1) and the shield braid to point D (S-1).
- At the other end of the cable, connect the center conductor to the center lug of the selected phono socket (S-1). Connect the shield braid to the ground lug of the same socket (S-1).
- On the outside of the rear panel, mark this socket "HFO."

### LMO MODIFICATION (SB Series Only).

Refer to Figure 3-9 (fold-out from Page 35) for the following steps.

 Cut a length of RG-174 coaxial cable (small which will reach from foil pad H on the bandpass circuit board to an unused phono socket, plus 1".

- ( ) Prepare the ends of the cable as before. See Figure 3-4.
- At one end of the cable, bend a small foot on the center conductor and solder it to foil pad H. Solder the shield braid of the cable to the hole in the foil at point E.
- At the free end of the coaxial cable, connect the center conductor to the center lug of the selected phono socket (S-1). Connect the shield braid to the ground lug of the same socket (S-1).
- ( ) On the outside of the rear panel, mark this socket "LMO."

#### VFO MODIFICATION (HW Series 5-Band Transceivers Only)

Refer to Figure 3-10 (fold-out from Page 35) for the following steps.

- Cut a length of RG-174 coaxial cable (small) which will reach from foil pad G on the bandpass circuit board to an unused phono socket, plus 1".
- ( ) Prepare the ends of the cable as before. See Figure 3-4.

  ( ) At one end of the cable, bend a small foot on the
- center conductor and solder it to foil pad G. Solder the shield braid of the cable to the hole in the foil at J.

  ( ) At the free end of the coaxial cable, connect the
  - center conductor to the center lug of the selected phono socket (S-1). Connect the shield braid to the ground lug of the same socket (S-1).
  - ) On the outside of the rear panel, mark this socket "LMO."

IMPORTANT: AFTER you have completed the modification of your equipment for the Frequency Display, and BEFORE you start the "linitial Tests," refer to the manual for your equipment and perform the alignment instructions for the HFO (high frequency oscillator) circuits. Also, be sure your equipment is operating normally on all bonds before startion the "(initial Tests."

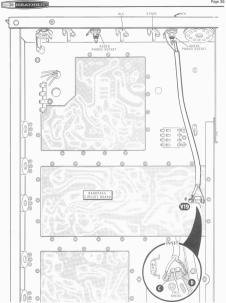


Figure 3-8 (HW-SERIES)

### RECEIVERS



Figure 3-11



Figure 3-1

NOTE: If the following Receivers (S8-300, S8-301, S8-303) are used in the transceive mode with the S8-400 or S8-401 Transmitters, the RF choke furnished must be installed in the transmitter. Refer to Figure 3-13 (Pictorial 10 in the S8-400 Manual, or Pictorial 4-2 in the S8-401 Manual).

- Bend each lead of an RF choke (#45-76) toward the slot in the choke winding form.
- Connect the RF choke between lugs 1 and 2 of the terminal strip (S-2). It may be necessary to melt the existing solder in the lugs before the choke leads can be inserted.
- Cut off the excess lead lengths, and check carefully to see that all connections to lugs 1 and 2 remain well soldered.

# SB-300 AND SB-301

- Refer to Figure 3-11 and install three phono sockets at the locations shown. Refer to Figure 3-1 for the hardware mounting detail.
- On the inside of the rear panel, refer to Figure 3-12 and connect a 100 Ω (brown-black-brown) resistor from the center lug of the HET OSC socket (S-1) to the center lug of socket A (S-1).
- Connect a 100 Ω (brown-black-brown) resistor from the center lug of the LMO socket (S-1) to the center lug of socket B (S-1),
- Connect a 100 Ω (brown-black-brown) resistor from the center lug of the BFO socket (S-1) to the center lug of socket C (S-1).

IMPORTANT: AFTER you have completed the modification of your requipment for the Frequency Display and BEFORE you start the "Initial Tests," refer to the manual for your equipment and perform the alignment instructions for the HFO (high frequency oxiditate) circuits. Also, be sure your equipment is operating normally on all bands before startion the "Initial Tests."



Figure 3-13

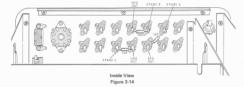
### SB-303

Refer to Figure 3-14 for the following steps

NOTE: A coaxial cable has previously been soldered to the back of each of the following three phone sockets. When you connect a resistor to one of these sockets, be sure to solder both the cable and the resistor leads to the luu.

- Solder one lead of a 100 Ω (brown-black-brown) resistor to the center lug of the HFO socket. Solder the other lead to the center conductor of the Spare B socket.
- Solder one lead of a 100 Ω (brown-black-brown) resistor to the center lug of the BFO OUT socket. Solder the other lead, to the center lug of the Spare A socket.
- Solder one lead of a 100 Ω (brown-black-brown) resistor to the center lug of the LMO OUT socket. Solder the other lead to the center lug of the Spare C socket.
- ( ) On the outside of the rear panel, mark HFO, BFO and LMO on the corresponding Spare A, B, and C sockets. Then proceed to "Initial Tests."

IMPORTANT: AFTER you have completed the modification of your equipment for the Frequency Display, and BEFORE you start the "Initial Tests," refer to the manual for your equipment and perform the alignment instructions for the HFO (high frequency collistor) circuits. Also, be sure your equipment is operating normally on all bands before starting the "Initial Tests."



### CAUTION

Use extreme care during initial testing and all subsequent maintenance of this Frequency Display. While this device is designed for maximum safety, never lose respect for the high voltage present in this unit. Protect yourself always against lethal or severe electric shock.

HEATH COMPANY

# **INITIAL TESTS**

The following tests are to make sure your Frequency Display operates properly. The frequencies displayed at this time are approximately correct; the actual calibration will be accomplished later.

These tests cannot be performed unless your equipment has first been prepared according to the modification steps in the "Installation" section of this Manual,

Refer to Figure 4-1 (fold-out from Page 36).

- On the under side of the chassis, temporarily solder each of the three red wires coming from grommets to the longer solder lug of the corresponding phono socket
- DANGER: Whenever the line cord is connected to an AC uniter, dangerous voltage will be present below the circuit board, even when the switch is in the OFF position. Except when an insulated tool is used, do not handle or work on this instrument when the covers are removed until the line cord has been disconnected. Refer to "High Voltage Areas" (fold-out from Pag-36).
- In the following tests, you will use the numbers indicated on the display tubes to check this unit for proper operation for you do not obtain the proper display in one of these steps, the areas and components most likely to cause your problem are listed after the step. If you are directed to check a component, and likely to cause your problem of the operation of the first problem of the proble
  - . Use the "General Troubleshooting Information,"
  - Refer to the "Troubleshooting Chart." If one of the "Symptoms" matches your difficulty, check the "Possible Causes" for that symptom.
  - Check the voltages against those given in the "Voltage Charts."

- 4. An oscilloscope is an extremely valuable tool for circuit analysis. Therefore, if a sensitive oscilloscope is available, use it for circuit tracing. Refer to the "Important Wave Shapes" on Page 54. Check the clock oscillator (102) for outqui, and follow the circuit path down through 109. Also check for the presence of an input signal, starting at the socket on the rear panel of the modified unit.
- () Connect the line cord plug of the Frequency Display to the AG source and turn the Display on. Lamy and V8 (kHz and decimal point) should light, together with the five right-hand display tubes. With two seconds the display tubes may flash once, after which each of the five tubes should display a "O" this display is not obtained, refer to the "In Case of DIFficial" vection of this Marcel and the Case of DIFficial"
- Position lamp V7 so it illuminates the "kH2" in the window of the front panel.
- Position lamp V8 so it is directly behind the small hole in the light shield; this acts as a decimal point.

### NOTES:

- In the following steps, the word "receiver" refers to your receiving equipment. The word "cable" refers to one of the three RG-58A/U cables prepared earlier.
- The term "random" display is used to indicate rapidly changing numbers in the display.
- A display of all zeros without a signal input has no significance as to whether the device is counting.

If there is a signal input and the display is all zeros, the device is not counting.

If there is a signal input and the display is random, the device is counting, but not correctly.

MEATHKIT<sup>8</sup> 39

- The displayed numbers are typed in this manual as they will appear on your Frequency Display,
- Secure the proper display in each of the following steps before proceeding to the next.
- ( ) Turn on your receiver and allow it to warm up for 10
- ( ) Turn the Band switch to 3.5.
- ( ) Set the Mode switch at LSB.
- ( ) Tune the receiver to zero beat its calibrator at 3700 kHz. Position the zero set line over the "0" on the circular dial. Then turn the calibrator off.

#### NOTES:

- In the following steps, be sure you make the cable connections on the receiver to the newly added or redesignated sockets ONLY.
- SB-303 owners: The LMO load, or a cable connection to a transmitter, MUST be connected to the original "LMO OUT" socket on the rear panel of your receiver.
- ( ) Connect a cable from the receiver LMO output socket to the Frequency Display HFO socket. The display should be 5 300,0 ±5 kHz and be stable. If this display is secured, proceed to the next step.
  - If the device does not count: Check the modification to your receiver; the connecting cable; Q1 and Q2; and IC's 10, 11, 12, 24, 25, and 32 through 35.
  - If there is a random count: Change the cable on the Frequency Display from the HEO to the LMO input. The display should now be 94 700.0 ±5 kHz, If this display is secured, and is stable, there is difficulty with the HFO input circuit. Check Q1, Q2, and IC24.

If the display is still unstable, the problem is probably in the clock circuit. If you secure a voltage of approximately .5 VAC at pin 11 of IC8, the clock oscillator (IC2) and IC3 through IC8 are satisfactory.

( ) Remove the cable and reconnect it from the receiver BFO output socket to the Frequency Display HFO socket. The display should be 3 393.6 ±5 kHz. and stable

If this display is not secured, the difficulty is probably in the receiver modification to the BFO output, in the

- cable connections, or the receiver MODE switch is in the wrong position.
- ( ) On the receiver, change the cable to the HFO output socket. The display should be 12 395,0 ±5 kHz, and stable
  - If this display is not secured, check the receiver modification for the HFO output, and the cable connections.
- ( ) Remove the cable.

NOTE: At this time, all three receiver outputs, and the HFO input of the Frequency Display, have been checked for satisfactory operation. The LMO and BFO inputs to the Frequency Display remain to be checked. ( ) Connect a cable between the receiver BFO output

socket and to the Frequency Display BFO socket, The display should be 96 606 4 +5 kHz

If the display is incorrect, move the cable from the BFO output socket to the LMO socket on the Frequency Display.

- If the display is now correct; Q3, Q4, IC10, or associated circuitry is at fault.
- If the display is still in error; check IC10, IC11, IC24 IC25, and IC's 32 through IC35. ( ) Leave the cable in the preceding step connected. Connect another cable from the receiver LMO output
- socket to the Frequency Display LMO socket, The display should be 91 304.0 ±5 kHz.
  - If this display is not correct, check Q5 and Q6, IC10, and IC32 through IC35.
- Leave the two cables connected and connect a third cable from the receiver HFO output socket to the Frequency Display HFO socket. The display should be 3 700.0 ±10 kHz, in which case the Frequency Display is operating properly.
  - If this display is not correct, check IC32 through 1035

NOTE: The frequency displayed will probably not be accurate at this time. However, it is important that the frequency is approximately correct and that, except for the right-hand digit, the display is stable

( ) Disconnect all cables and disconnect the Frequency Display line cord plug from the ac power source,

This completes the "Initial Tests." Turn to "Circuit Board Shield Installation,"

# CIRCUIT BOARD SHIELD INSTALLATION

Refer to Pictorial 4-1 (fold-out from Page 43) for the following steps,

- ( ) Unsolder the three red wires from the three phono sockets
- ( ) Position the three red and the three blue wires so they point straight up from the circuit board.
- ( ) Refer to Detail 4-1A and mount three 2000  $\Omega$  controls on the circuit board shield at CL, CN, and CP, NOTE: It is possible to mount the controls on the wrong side of the shield. Be sure the large cutout in the corner of the shield is positioned as shown in the Pictorial Place the controls so the three solder lugs point to the closer edge of the shield. Secure the controls by bending the lugs over with pliers or a screwdriver.

Refer to Detail 4-1B (fold-out from Page 43) for the following three steps:

- ( ) Place the circuit board shield against the bottom of the chassis so it covers the bottom of the circuit board. The three cutouts near the controls must clear the three grommets. Position this edge under the chassis lip first. Route the white wires out from under the shield at the side of the chassis and at the front panel edge.
- ( ) Secure the circuit board shield to the bottom of the chassis at AW, AY, BF and BJ, Use #6 x 1/4" sheet metal screws.
- ( ) Refer to Detail 4-1C and install plastic cable clamps at A.I and A.I. Slip the clamps around the two white wires and secure each with a 3/8" OD flat washer and a #6 x 3/8" sheet metal screw.
- ( ) Connect the red wire coming from grommet BE to lug 2 of control CP (S-1).
- ( ) Connect the red wire coming from grommet BG to lug ( ) Blue wire from phone socket C to lug 3 of control CP 2 of control CN (S-1).





- ( ) Connect the red wire coming from grommet BK to lug 2 of control CL (S-1).
- ( ) Prepare the following lengths of red wire:

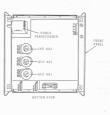
One

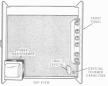
1.1/4"

- ( ) Connect the 1-1/2" red wire from the longer lug of phono socket E (S-1) to lug 1 of control CL (S-1).
- ( ) Connect a 1-1/4" red wire from the longer lug of phono socket D (S-1) to lug 1 of control CN (S-1).
- ( ) Connect a 1-1/4" red wire from the longer lug of phono socket C (S-1) to lug 1 of control CP (S-1).

Connect the three blue wires coming from the phono encleate as follows:

- ( ) Blue wire from phono socket E to Jug 3 of control CL (8.1)
- ( ) Blue wire from phono socket D to lug 3 of control CN





PICTORIAL 4-2

# CALIBRATION

#### Refer to Pictorial 4-2 for the following steps.

- On the bottom of the circuit board shield, turn the three controls fully counterclockwise.
- Use the three previously prepared cables and connect the HFO, LMO (or VFO), and BFO receiver outputs to the corresponding input sockets on the rear panel of the Frequency Display.
- Plug the line cord of the Frequency Display into an AC receptacle.
- Turn on the Frequency Display and your receiver and let them warm up for at least 10 minutes.
- Set your receiver mode switch to USB, the band switch to 29.5, and the circular DIAL to 200.

NOTE: This calibration procedure adjusts the signal amplitude to the Frequency Display so it will not be overdriven.

A "stable" display is defined as one in which the display is unwarying at any receiver dial position. The right hand display tube may vary by one or two digits. If the display ends in 9, 1, or 0, this variation may also cause the fifth tube to vary by one digit (one kHz).

- Turn the LMO ADJUST control in a clockwise direction; the display may pass through an erratic area and then stabilize at 94 700.0 ±5 kHz. Then turn the control slightly more (about 1/16 of a turn).
- Turn the BFO ADJ clockwise in the same manner as in the preceding step until the display stabilizes at 91 305.0 ±5 kHz. Then turn the control slightly more.
- Turn the HFO ADJ clockwise until the display stabilizes at 29 700.0 ±10 kHz. Turn the control slightly more.

NOTE: The following step requires that an accurate calibration signal be received. In the U.S.A. or Canada use stations WWV (15,0000 MHz) or CHU (7,33500 MHz).

Otherwise, use a commercial station of known frequency occuracy, NOTE: The SB-301 recipier will receive Way.

15 MHz, but the Frequency Display will not display the frequency as this receiver was designed so the HFO outlet on cot switched to the rare panel socket when the band switch is in the 15 MHz position. This was done to previously inadventent transmission on the WWV frequency when transceiving.

If the above is impossible with your equipment, it will be necessary to adjust your calibrator (using the directions in your equipment manual) and then calibrate the Frequency Display against your calibrator.

[ ] Zero beat your receiver to a radio station whose frequency is known to be accurate. If the frequency of the station is correctly displayed, no further calibration is required. If the display is not correct, perform the two following stors:

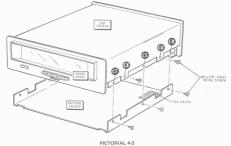


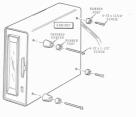
- Refer to Detail 4-2A and use a pair of pliers to push the 1" steel blade into the smaller end of the nut starter until 1/8" remains exposed.
- ( ) Refer to Pictorial 4-2 and use the alignment tool to adjust the crystal trimmer capacitor so the display
- ( ) Straighten the display tubes. Pass the large end of the nut starter through the grommet over each tube.
- ( ) Unplue the line cord of the Frequency Display.

This completes the "Calibration" of your Frequency Display, Proceed to "Final Assembly,"









PICTORIAL 4-4

# FINAL ASSEMBLY

#### Refer to Pictorial 4-3 for the following steps.

- ( ) Refer to Detail 4-3A and install the remaining piece of fish paper on the bottom shield. Remove the white protective covering, position the paper as shown with the 2" edge along the chassis lip, and rub the paper into place.
- Attach the bottom shield to the chassis. Use six #6 x 1/4" sheet metal screws at CR, CS, and CT, and at the three corresponding holes on the other side of the chassis. Be sure the fish paper is positioned over terminal strips BB and BC (fold-out from Page 27).
- Attach the top shield to the chassis. Use four #6 x 1/4" sheet metal screws at CU, CX, and at the two holes on the other side of the chassis.
- Slide the Frequency Display into the cabinet so the four Speed Nuts on the corners of the chassis match the four holes in the bottom of the cabinet.

NOTE: At this time decide whether you want the cabinet of the Frequency Display to set level, or whether you wish the front of its cabinet elevated. Refer to Pictorial 4-4 and perform only <u>one</u> of the two following steps, depending upon how you want the cabinet positioned.

 If you want the cabinet to sit level, install a rubber foot at each corner of the cabinet. Use 6-32 x 11/16" screws.



Detail 4-3

 If you wish the front of the cabinet elevated, install a rubber foot on each cabinet rear corner with a 6-32 x 1/16" screw. Install a tapered spacer and a rubber foot at each cabinet front corner with a 6-32 x 1-1/2" screw.

This completes the assembly of your Frequency Display. Proceed to "Operation."

# 선물명 하는 이 요리 그리 사람이

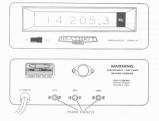


Figure 5-1

# **OPERATION**

Front and rear views of the Frequency Display are shown in

#### OPERATING CONTROLS

The front panel switch turns the unit on and off. It is the only operating control.

#### INTERCONNECTIONS

#### SB-100, SB-101, SB-102, HW-100, and HW-101

The Frequency Display is connected to the three phono sockets on the rear panel as referred to in the "Installation" section of this Manual.

### SB-300, SB-301, and SB-303

Any existing connections between these Receivers and your other equipment will remain. The Frequency Display will be connected to the three phono sockets as referred to in the "Installation" section of the Manual.

#### DISPLAY STABILITY

When the Display is first turned on, it may momentarily display a random number. The display will usually stabilize within two seconds and is then updated more than six times each second. Best display stability occurs after at least ten minutes warmup time.

Depending largely upon the exact frequency to which the receiver is tuned, the right hand display tube (hundreds of Hertz) may alternate between two numbers. This is normal. If the receiver is tuned very close to a frequency ending in 0 or 9 kHz, the second tube from the right will also vary as the right-hand display moves back and forth between the two

#### FRRONEOUS READINGS

#### SB-100, SB-101, SB-102, HW-100, and HW-101

When you operate in the CW mode, the Frequency Display will indicate the receiver frequency. The 1 kHz offset (BFO shift) will be indicated with the key down. During other operation; both the transmit and receive frequencies will be indicated.

#### SB-300, SB-301, and SB-303

The BFO signal is not present when the MODE switch is in the AM position. The display will read high by the BFO frequency.

In the RTTY mode of the SB-301 and SB-303, the BFO

When these receivers are used with a transmitter in the transceive mode, the transmit frequency will be accurately displayed only if the same three oscillators are used in <u>both</u> transmit and receive modes.

#### CAUTION

Do not position the Frequency Display on top of a transmitter, or other heat-producing equipment. The specified operating temperatures may be acceeded (see "Specifications"). If such a location is unavoidable, use a heat buffer such as a sheet of cork or abstess ounder the Frequency Display, but do not block off the flow of air around the unit.

# IN CASE OF DIFFICULTY

This five-part section gives suggestions for locating and resolving difficulties,

The first part, "General Troubleshooting Information," deals with difficulties which exist upon completion of the assembly of your kit, and is primarily directed to soldering and assembly problems.

The second part consists of a "Troubleshooting Chart," which gives difficulties and likely causes.

The third part "Voltage Charles" talls are best to shoot

The third part, "Voltage Checks," tells you how to check the power supply voltages, and includes Voltage Charts that show typical voltages at all IC's and transistors. The fourth part, "Important Wave Shapes," contains charts with significant waveforms.

The "Customer Service" information inside the rear cover of the manual tells you what to do if you want to take advantage of the help available from the Heath Company.

If your check for soldering and assembly difficulties does not locate the problem, the difficulty is probably a component. Read the "Circuit Description" (Pages 67 through 73), refer to the Schematic Diagram (fold-out from Page 85), and use the signal path drawings (Pages 69 through 72).

# GENERAL TROUBLESHOOTING INFORMATION

- Be sure the proper power was applied to the transformer primary; check the wiring.
- Most problems result from poor connections and soldering, Lus a reading plass and check all solder connections to be sure they are soldered as described in the "Soldering" section of the "KiB Builders Guide." Also check for bits of solder, wire ends, or other foreign matter which may be lodged in the writing, Lock for solder bridges between clicust board foils. Compare your foil pattern with the "X-Ray Vitens" on Page 81.
- Chick the continuity of the circuit board folia, including those places where a folir runs through a hole to connect as the plant all bottom for the opportunity of the plant and bottom folir polythm. (Such as fade may also be used for mounting a component, as fade may also be used for mounting a component, as fade and a fade and



NOTE: If a component lead (such as an IC terminal) is mounted in a hole through which the foil passes from the top to the bottom of the circuit board, place one of your probes on the component lead on top of the board. In this way, an open foil in the circuit board hole can be discovered.

- Check the transistors and integrated circuits. See that the correct component is installed and that the leads are in the correct holes in the circuit board. Refer to the step-by-step Pictorials.
- Check the values of the parts. Be sure the proper value has been wired into the circuit.
  - Press each integrated circuit firmly into its socket so that each pin will make a secure connection.

#### SUBSTITUTION

Corresponding components of the circuitry for each tube can be interchanged with the components of another tube. IC's 12 through IC17 can be interchanged, for example,

If one display tube shows two digits simultaneously, interchange with one of the other rubules to determine interchange with one of the other rubules to determine the tube or the circuit is faulty. If the circuit is faulty, if the tube or the circuit is faulty, if the tube or the circuit is faulty and there are no solder bridges on the secondate foil, interchange the decoder/driver IC with one of the others. This method can be used with other single digit problems and can be extended to interchanging torage registers and up/down counter intercented circuits.

#### COUNTING AND DISPLAY CIRCUITS (Figure 5-2)

A counting and display circuit consists of the up/down counter and the associated storage register, decoder/driver, and display tube. A high input impedance voltmater can be used to check the logic states of only the storage registers, decoder/drivers, and display tubes. For these voltage checks, a "high" is 2.4 VDC or more, whereas a "low" is .4 VDC or less.

The up/down counters cannot be accurately checked with a voltmeter, as their outputs change rapidly with the count. The substitution method described above is one way to check them.

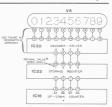


Figure 5-2

\*Add the values of the highs to determine the decimal equivalent. For example, if pins 16 and 9 of 10:22 are high (pins 15 and 10 remain low), then the decimal equivalent is 5 (144-95). If only pin 10 is high, the decimal equivalent is 6. If all pins are low, the decimal equivalent is 0.

#### EXAMPLE (Figure 5-2)

- If tube V5 displays a "5" when you know a "3" should be seen (80 metre band), transpose IC16 (in this example) with one of the other up/down counters whose display is cornect. HV Still displays a "5"," it is reasonable to assume the IC is operational. If the display change to a "3"," the IC is probably but, although the original IC should be inserted in its socket again to make sure.
- If the display remains a "5," check the outputs of the storage register, IC22. If pins 16 and 9 are high and pins 15 and 10 are low, then the storage register is probably good (1+4-5).
- The decoder/driver, IC30, should have pin 14 low to turn on the 5 in the display tube, and all other output pins should be high. If pin 9 should be low, which turns on the 3 in the display tube, but a 5 is displayed, then the fault is probably in the tube and it should be substituted with one of the others as confirmation.
- If the foregoing checks are all indicative of a "5," then it is reasonable to assume that the difficulty lies ahead of the counting and display circuits.



# TROUBLESHOOTING CHART

SYMPTOM	POSSIBLE CAUSE
No lamps or display tubes light.	Power supply faulty or no power to unit.     No voltage on transformer primary.     Transformer.     Fuse blown.     A Svolt line shorted.     It you'll ne shorted.     It you'll ne shorted.     It you'll ne shorted.
kHz and decimal lamps (V7 and V8) light; digits not lit.	IC1.     Rectifiers D1, D2, D3, or D4.     5 volts missing.
Displays 80 000.00 ail of the time (doesn't count).	1. 12 volt missing.     Clock not running: Meter needle should oscillate on workages (see voltage dwarf for ICB and ICD) when clock is running.     R. ICZ brough ICD.     R. ICZ brough ICD.     R. ICZ brough ICD.     A. ICZ reversed in socket, (in this cas, display may have locked in on any number.)
One or more, but not all, display tubes show errong number.	The associated readout circuitry; display tube, decoder/driver, storage, and up/down counter integrated circuits; (which are physically in line behind each display tube).
One digit locked on one number; will not follow LMO tuning.	The associated readout circuitry; decoder/ driver, storage, and up/down counter integrated circuits (which are physically in line behind each display tube).



SYMPTOM	POSSIBLE CAUSE
More than one digit lights in one display tube.	Solder bridge across foil.     Associated decoder/driver IC,     Bent pin on readout tube,     Display tube.
Display remains at 99 999.9.	1. IC25.
Total display complete blur.	1. IC35.
Unit counts HFO, does not subtract LMO and BFO.	1. IC9 or IC35.
Unit counts erratic on 21 MHz band or higher.	1. IC12, 24, or 25.
Unit suddenly becomes erratic after it has been working properly.	Ambient temperature too high.     Observe the proper operating temperature specifications.
Unit operates well on one or more 10-meter positions but is erratic on one or more of the other positions.	HFO alignment in receiver incorrect. Align and readjust where necessary.
Random diaplay, diapl	EGZ per ICGZ.     Clock not number; Meter needle should oscillate on Avoltage; few voltage draft for ICB and ICB when clock is running.     A ICC reserved in sedent, fin bodded in on any number; looked in on any number;     E. Defective crystal,     C. ICZ through ICB.
Random display; frequency adjust control sensitive.	1. IC2.

#### VOLTAGE CHECKS

### SUPPLY VOLTAGES

Three supply voltages (170, 5, and 12 volts) are required to operate this unit. If the decimal and kHz indicator lamps (V7 and V8) light, the 170 volt supply is functioning. If the six readout tubes light, the 5 volts must be present. If a signal at any of the inputs is counted, 12 volts is present at that input.

If these voltages are not present, measure the cathode of D5 for 170 volts, the cathode of D6 for 12 volts and the wire passing through each ferrite bead for 5 volts.

Measure the voltage at the Vcc pin of each IC on the shoulder of the pin where it enters the case.

Typical supply and signal voltages (measured with a high impedance voltmeter) are given on the Voltage Charts. The A symbol on the chart indicates the meter needle will oscillate (move quickly back and forth over a narrow range). The symbol indicates a voltage range from 2.5 to 90 volts.

If you interchange two IC's and the display does not change, assume neither IC is at fault. If the display changes, assume one or both IC's are at fault.

#### VOLTAGE CHARTS



A 0.5

2.04

### VOLTAGE CHE

Measure

Typical

impedan

symbol

symbol i

If you in

assume r

one or b

IDENTIFICATION DRAWING

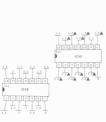
#### SUPPLY VOLTAGES

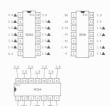
Three supply voltages {170, 5, and 12 volts} are required to operate this unit. If the decimal and kHz indicator lamps (V/7 and V8) light, the 170 volt supply is functioning. If the six readout tubes light, the 5 volts must be present. If a signal at any of the inputs is counted, 12 volts is present at that input.

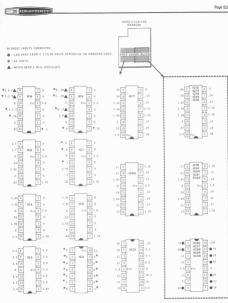
If these voltages are not present, measure the cathode of D5 for 170 volts, the cathode of D6 for 12 volts and the wire passing through each ferrite bead for 5 volts.

# VOLTAGE CHARTS

A:WETER NEEDLE WILL OSCILLATE







### IMPORTANT WAVE SHAPES

This section consists of the significant wave shapes present at IC's other than those in the six counting chains. For information about the latter, see "Counting and Display Circuits" on Page 49.

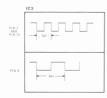
A 100 MHz bandwidth oscilloscope is desirable to properly reproduce these wave shapes. Those presented here were obtained by using a Tektronix Model 454 with a low capacity X10 probe. The oscilloscope was set for  $\delta$  volts-per-division. Time is indicated in  $\mu$ s (microseconds) and ms (milliseconds).

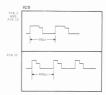
Filled-in waveforms indicate a sine-wave input signal being counted. If the signal input is removed, only the square waveform will remain,

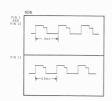
NOTE: The lowest point of each of the following waveforms is 0 volts, whereas, the highest point is 2.4 to 5 volts.

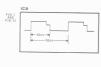


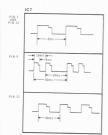






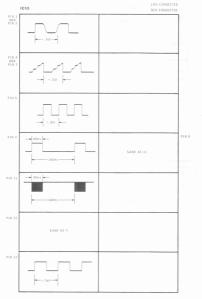


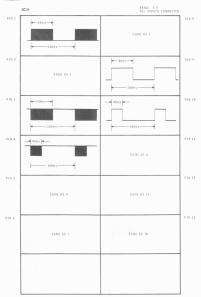


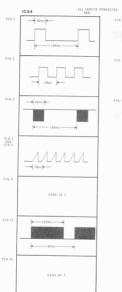


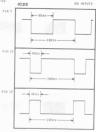


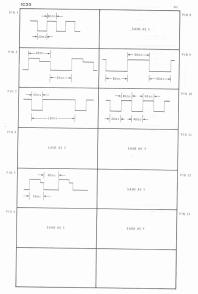


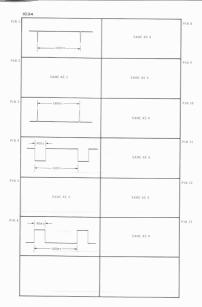


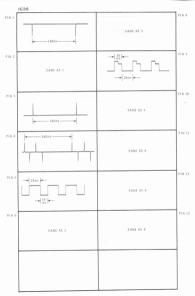












# **SPECIFICATIONS**

3 to 40 MHz. 6 display tubes. 30 feet. 5 volts rms. Within 100 Hz ±1 count after 20 minute warm up. 160 milliseconds. 2000.0 Internally generated spurious < .25 µV equivalent signal level. Crystal (clock) frequency . . . . . . . . . . . . . . . . . . 1 MHz. <10 ppm/yr. <10 ppm from 10 to +65 degrees C. 0 degrees to +40 degrees C. -55 degrees to +80 degrees C. 105-125 or 210-250 VAC 50/60 Hz 15 watts 10" wide, 10-1/4" deep, 4" high.

> The Heath Company reserves the right to discontinue instruments and to change specifications at any time without incurring any obligation to incorporate new features in instruments previously sold.

4-1/2 lbs.

# SPECIFICATIONS

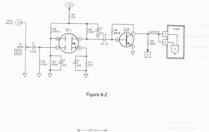




Figure 6-3

# CIRCUIT DESCRIPTION

While you read this description, refer to the Block Diagram (Figure 6-1 fold-out from Page 64) and the Schematic Diagram.

The Frequency Display receives signals from the three conciliators of a Heath ameteur receiver or Shand transceiver operating below 30 MHz, an up/down counter counts upon the HFO likely frequency collistor) frequency and then counts down to subtract the frequencies of the LMO (linear matter oscillator) and the BFO (beat frequency collistor). The result is the operating frequency, which is displayed by a illuminated total as of MHz are the post of the count frequency control of the country of the

	kHz
High frequency oscillator (count up)	12 395.0
Linear master oscillator (count down)	-5199.5
	7 105 5

Beat frequency oscillator (count down) — 3 393.6
Operating frequency (display): 3 801.9

The frequency displayed is recalculated once every 180 millibeconds, or approximately six times each second. Each 180 millibeconds eyele is further divided into four parts of 40 millibeconds each. Three of these four parts are used by the counting cycles, and the fourth is used for the processing and display of the count.

In order to properly describe each counting cycle, the action is assumed to be "frozen" at a point in time. Connections not utilized in the function being described are not shown in the illustrations.

### INPUT CIRCUITS (Figure 6-2)

Except for the  $\Omega 2$  bissing, each of the three input circuits is identical, differing only in the gate to which the output is connected. Therefore, only the HFO (high frequency oscillator) input circuit will be described.

Control R1 adjasts the level of the input signal to 0.1, a MOSFET which has shiply input impedance to avoid long in the signal source. Other resistors and capacitors associated with 0.1 are the usual blassing, loading, and bright operations components. The input signal is amplified and coupled to 0.2, which interfaces the output of 0.1 to digital logic levels for the following circuits. R8 and R9 form a blassing network for 0.2.

### CLOCK AND FREQUENCY DIVIDER

The clock oscillator is formed by IC2, and 1 MHz crystal Y1, and the associated parts. C31 is a trimmer capacitor to adjust the crystal to the exact frequency.

The 1 MHz output frequency of IC2 is divided by IC3 through IC9 (the clock frequency divider) as follows:

	FUNCTION	OUTPUT	
IC3	+4	250 kHz	
IC4	÷10	25 kHz	
(15) IC5	. \ +10	2.5 kHz 2.50 kH	
68/1C6	±10	250 Hz 2.377	
IC7	+10	25 Hz 25 NW	
IC8	÷2	12.5 Hz 25H 16 see	
100	-2	6 25 Hz 1 1 2 2	

The four outputs of the clock frequency divider come from IC7, IC8, and IC9 as shown on the Schematic. These outputs provide the timing for the instrument.

Figure 6-3 shows the relationships between the output waveforms of ICB and ICB during one cycle of ICB, which is 100 milliseconds long (1 second ÷ 6.25 herz). The four periods are each 40 milliseconds long and are designated t<sub>1</sub>, t<sub>2</sub>, t<sub>3</sub> and t<sub>4</sub>. The functions which occur during each time period are indicated.



Figure 6-4

#### LOGIC CIRCUITRY

The gates used in this instrument are NAND type only. However, the gates used differ as to operating speed and frequency range.

Figure 6-4 is the truth table for a two-input NAND gate. The various combinations of logic levels at gate inputs A and B will result in the output logic levels shown in the OUT column. In this discussion, the terms "high" and "low" are often used to describe logic 1 and logic 0 levels, respectively.

In the first three lines of the tunk belo, wherever a gat be also folgo of at a rejunct the receptor will serve to a likely folgo: 1. That inhibits follows the test was the large following the server and the server a

Logic levels are shown at the inputs and outputs of most of the gates in this "Circuit Description." When levels are shown as "0/1", it means that the logic level is changing back and forth at the signal frequency. The symbol  $\frac{1}{2}$  crossing the input line to an IC means that the IC will toggle (change output states) when the trailing edge of the input waveform appears, while Jmeans that the device will toggle when the leading edge of the waveform appears,

### SEQUENCER AND MULTIPLEXER CIRCUITS

The function of these circuits is to process the output logic of the clock frequency disidire or has the three input signals are selected in the correct sequence and routed through the proper channels to the up/down counts of the correct sequence and proper channels to the up/down counts of the correct sequence and injusts into digital logic voltage levels. In each case, the gas following the transistor aids in the proper shaping of the signal waveform.

Figures 6.5, 6.7, and 6.8 are identical as to the devices hown. They differ as to the signal source, the signal path, and the control logic from the outputs of 10 Sand ICD. The signal source is shown in a box in each Figure and the signal path is shown in heavy lines from the source box to the input of the up/down counters, ICI2, Pin 5 of ICI2 is the count-up input and pin 4 is the count-down input. Each figure shows the specific waveform relationship of ICB and ICB for the function lituarytes/d.

The three oscillator inputs are always present, but all are inhibited by gates during  $t_a$ . Only the proper signal is permitted entry during the other three time periods, Fig. 6-9 shows the logic levels during  $t_a$  when there is no counting.

The up/down counter has an upper frequency limitation and IC25 performs a divide-by-four function to bring the 10 meter band HFO signals within the counter's operating range.

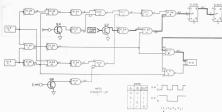


Figure 6-5

#### HFO INPUT (Figure 6-5)

During the time the HFO input is counted, logic levels of time  $\mathbf{t}_1$  apply and the HFO count-up function occurs.

The signal input is through Q2 and along the heavy lines to pin 5 of IC12, the count-up input of the up/down counter. The logic levels (see the truth table) from the outputs of IC8 and IC9 control the gates so only the signal from the HFO input is counted.

The signal from the BFO (Q4) is denied entry to the counter because IC10D is inhibited by a low at one of its inputs. Likewise, the LMO signal (Q6) is denied entry because IC10B is inhibited by a low.

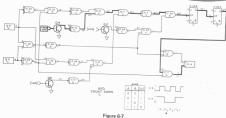
The HFO signal can then be processed because one input of IC24A is held high (by IC24B) and its output then alternates at the frequency of the signal at its other input. Gate IC24B is open because one input is held high by IC11A.

The flip-flops used are devices composed of interconnected gates and other components housed in an IC package. As connected, a negative-going signal at the injurt will cause a change in the logic level of the outputs. A positive-going input signal has no effect. Thus, each flip-flop divides its input frequency by two, as zeen in Flaure 6-5.

IC25, as it contains two flip-flops, performs a divide-by-four function. The signal path is then through IC11B (as one input is held high by IC34B) to pin 5 of IC12. The signal cannot pass through IC11D because it is inhibited by the low from IC32C.



Figure 6-6



# BFO INPUT (Figure 6-7)

## LMO INPUT (Figure 6-8)

The BFO input is selected by the logic levels of IC8 and IC9 during time t; The signal path is through O4 and along the heavy lines to IC25. Gate IC24A in the HFO signal path is now inhibited by a low at one of its inputs, as is IC108 in the LMO signal path.

During time t<sub>3</sub>, IC8 and IC9 open the signal path from Q6, and inhibit gates IC24A and IC10D in the signal paths from Q2 and Q4, As in the case of the BFO input, the signal moves through IC11D to pin 4 of IC12.

The signal path from IC25 is through IC11D to pin 4 of IC12, the count-down input of the counter.

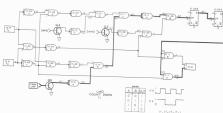
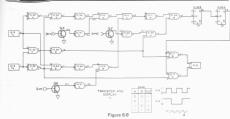


Figure 6-8



### TRANSFER AND STORAGE

During time t<sub>4</sub>, the IC8 and IC9 output logic causes getes IC24A, IC10D, and IC10B to be inhibited as shown in Figure 6-9. This prevents the three oscillator signals from reaching the up/down counter.

The logic output at pin 8 of IC7 changes midway during  $t_a$  to divide the time into two 20 millisecond periods. The output is high during the first period and low during the second. See Figures 6-10 and 6-11.

Figure 6-10 shows how the transfer pulse is generated for the storage registers of the up/down counter during the first 20 milliseconds of T<sub>4</sub>. Pin 8 of IC7 is high and holds gate IC35B open. The outputs of IC8 and IC9, through a series of gates, apply a high to one input of IC32D to hold the gate open.

From pin 9 of 107, two cycles of a square wave are applied to one input of 10220. These waves appear at the gate output and are differentiated by C33 and R41 to form a positive-going pales at the input of 10258. A spit 10258 is held open, the input is inverted and a negative-going pube appears at the gate output. This pales is inverted by agreement of 10256 to a positive pube which is fed to 1018 through 1023 as at he puble which commands the transfer of the count from the up(down counter to the storage registers. (See the "Counteing and Diagney's rection follows).

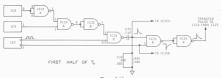


Figure 6-10

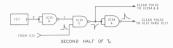


Figure 6-11

Figure 6-11 shows how the clear pulses are derived for the up/down counters and for IC25. These pulses return the counters and the flip-flop to zero, ready for the start of the next count.

During the second half of t<sub>4</sub>, pin 8 of IC7 is low. The inversion to a high by IC350 holds IC35C open, so it passes

and inverts the pulse from C33 (Figure 6-9). The negative-going pulse from IC35C is used as the clear pulse for IC25, whose Q outputs are thereby returned to 0, ready for the next counting cycle.

The negative pulse from IC35C is inverted by IC34A to a positive pulse which is used as the clear pulse to return IC12 through IC17 to zero.

### COUNTING AND DISPLAY

At the beginning of t<sub>s</sub>, the transfer and storage period, the quidone court has been completed and is held in the counters. The court is in binary-cooled decirate, or BCD<sub>1</sub>, as multiplied point may be the state (in and 11. The deciral system uses the tasks, binding in the state (in and 11. The deciral system uses the tasks, binding in the control correspond to the two states of a winth— one or closed. A digit in the binary system (0 to 1) is called a "bit," and in mort closes now be as required to present a quantity than is nocessary in the deciral system, rhower, for logic disclosures of the binary system counterful in disclosures of the binary system counterful in disclosures that the control of the binary system counterful in the control of the binary system of the binary system the control of the binary system of the binary sy Each up(drown counter has a 4-bit BCD output. For a few microscoord, during t,, the inputs to the storage register are opened by a transfer poles, and the state of the completed binary count is stransferred to the storage register, which holds the count until it receives another transfer poles after the next count is completed. The principal reason for using a storage register is that the display is held steady until the next count is completed. The principal reason for using a storage register is that the display is held steady until the next count is completed. The principal reason for using a constant count has been completed. The principal reason for using during safe count, and the display would actually remain constant for only a sumil frestion of a second.

The count held by the storage registers appears at the input of the decoder-driver, which decodes the binary count and turns on the proper number in the display tube.

#### Up/Down Counters

The countries used are synchronous, 4-bit up(form countries. The four coupts of side are in the 1.2 4 8 or "instaut" binary code, Figura 3 in the "Chimmits Apparella" above a tool codigation of the country, authentic deligenma, partial local coding and the country, authentic deligenma, partial tensis which the device will recognize as a high or flow. Note that the countries of the countries of the countries of low-to-high level transition of either input. Pin 5 is the countries private and on 4 is the countries private and on 4 is the countries of the countries of circuits on the countries of the countries of circuits to the countries of the other countries of the other countries of the coun

#### Storage Registers

The four binary outputs of an uniform counter are always present at the four inputs of a stronge register, which is a 46-bit letch. The information present at a soci listch input; at 46-bit letch. The information present at soci listch input; at 46-bit letch. The information present at soci listch input; and input input input in put in

#### Decoder-Drivers

These devices receive the binary count present at the Q outputs of the storage registers and decode the count of outputs of the storage registers and decode the count count of the ten decoder-driver outputs is grounded. This results in the Illumination of the correct number in a display the Figure M of the "Schematic Appendix" contains the information necessary to relate the binary input to be output logic levels required to turn on the proper display tube number.

### Display Tubes

The amode (prins 7 and 10) of each display tubb is connected to the 170-volt DC supply line through a 10 kiQ current limiting resistor. The decimal point connections of each of the records (prins 13 and 14) were out off earlier. Each of the remaining 10 pins is connected to a tube cathode, such cathode being shaped as one of the numbers 0 through 9. When one of these cathode is grounded (field low) by the decoder-driver, the gas immediately surrounding the cathode will logize and the activide will lonize and the activities will lonize activities and the activities will lonize activities and the activities will lonize and the activities and the activities will lonize activities and the activities will lonize activities and the activities will lonize activities and the activities activities and the activities activities and the activities activities and the activities activities activities and the activities activities and the activities activities activities and the activities activities and the activities activities activities activities activities and the activities activities and the activities activities activities activities activities activities and the activities acti

The cathodes held high by the decoder/driver outputs will not glow.

Neon tubes V7 and V8 (kHz and decimal point illuminators) are turned on and off at the same time as the display tubes.

#### POWER SUPPLY

The AC operated power supply can be wired to operate

One secondary winding of the power transformer is connected to diode D5 which acts as a half-wave rectifier for the 170-volt DC supply. This supply is filtered by C21 and R45.

The other secondary winding of the power transformer supplies two voltages:

The output of a bridge rectifier composed of diodes D1, D2, D3, and D4 is filtered by C19 and R29 and is regulated by zener diode D8 to furnish 12 VDC for the three MOSFET transistors Q1, Q3, and Q5.

Half of the winding in the preceding section is filtered by C17 and C18 and is processed by a voltage regulating circuit contained in IC1. Refer to Figure E of the "Schematic Appendix" for the schematic of this IC. This circuit furnishes a regulated 5 volts DC for the digital circuits and Q2. Q4, and Q6.

The three ferrite beads and capacitors C26, C27, and C28 on the three IC1 output lines are used to suppress vhf transients.

# SCHEMATIC APPENDIX

This appendix to the schematic diagram presents information concerning the display tubes and the solid state devices contained in your kit. Refer first to Figure A, below.

Then turn to the Figure number designated for each device for additional information, drawings, waveforms, and truth tables as appropriate.

1	COMPONENT DESIGNATION	PART NUMBER	QUAN- TITY	TYPE and RATING	FIGURE NUMBER
ı	D1, 2, 3, 4	56-65	4	1N4002, 1A, 100 PIV	None
I	D5	57-27	1 .	1N2071, 1A, 600 PIV	None
I	D6	56-57	1	1N716A, 12V Zener, .4W	None
I	Q1	417-274	1	40673 (selected)	В
1	Q3, 5	417-240	3	40673	В
I	02, 4, 6	417-154	3	2N2369	С
-	V1 – V6	411-284	6	National Electronics NL1220 or Burroughs 8-5859A	D
I	IC1	442-30	1	UA309K	E
-	IC2, 10, 32, 33, 34, 35	443-1	6	SN7400N	F
	IC3	443-5	1	SN7473N	G
Name and Address of the Owner, where	IC4, 5, 6 7, 8, 9	443-7	6	SN7490N	н .
ľ	IC11, 24	443-71	2	SN74H00N (High speed)	F .
	IC12, 13, 14 15, 16, 17	443-66	6	SN74192N	J
	IC18, 19, 20, 21, 22, 23	443-13	6	SN7475N	К
Γ	IC25	443-70	1	SN74H103N	L
	IC26, 27, 28, 29, 30, 31	443-35	6	SN7441B	М



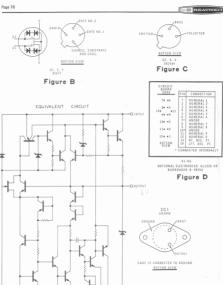


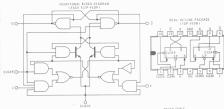
Figure E



LOGIC LEVEL VOLTAGES			
	MINIMUM	TYPICAL	
SATE INPUT-HIGH SATE INPUT-LOW SATE OUTPUT HIGH	2V 2.4V	3.3V	0.87
SATE OUTPUT-LOW		.22V	0.4V

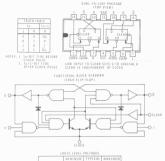
HEATHKIT

# NAND Figure F



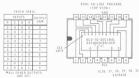
	LEACH FLIP-E		TP-FLOP
	NP		001901
L	1	K	Q
Г	0	0	0
	1	0	0 <sub>n</sub>
		1 1	0 1

Figure G



1 2.4V 3.2V 0.25V 0

# Figure L



NOTE: THESE ARE THE DISPLAY TUBE NUMBERS WHICH WILL BE TUBEND ON BY THE 10 DECORE-ORIVER OUT-PUTS. THE ACTIVATE OUT THE WILL BE ""MIN AND THE OTHER WINE OUTPUTS "HIGH". FOR EXAMPLE, IF THE NUMBER "IT IS LILLUMINATED IN THE DISPLAY TUBE, PIN 10 OF THE ROTHER THE OUTPUT THE WILL BE "COM" AND THE OTHER WINE OUTPUT PINS WILL BE "COM" AND THE OTHER WINE OUTPUT PINS WILL BE

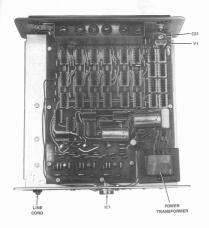
	MINIMUM	MAXIMUN
NPUT-HIGH	27	
NPUT-LOW		0.8V
TPUT-HIGH	2.5V	90.0V

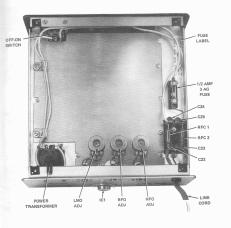
# Figure M

DECODER BOOK DECIMAL

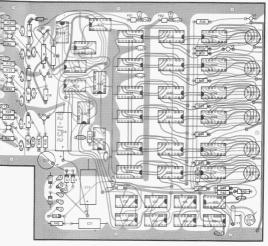


### CHASSIS PHOTOGRAPHS



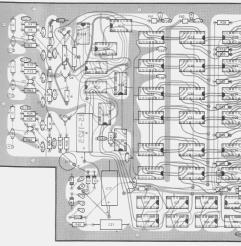


### CIRCUIT BOARD X-RAY VIEWS

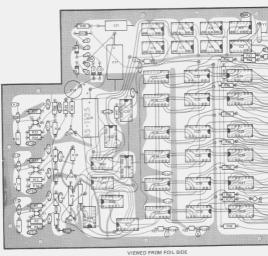


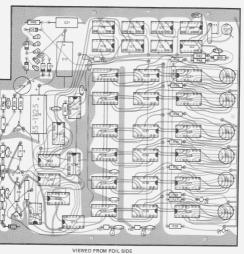
VIEWED FROM COMPONENT SIDE

# CIRCUIT BOARD X-RAY



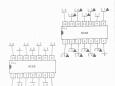
VIEWED FROM COMPONENT SIDE

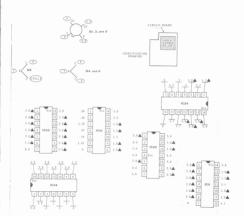






### A METER NEEDLE WILL OSCILLATE.

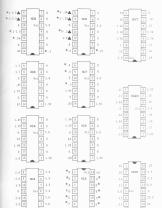




# WITHOUT IMPUTS CONNECTED.

- . CAN VARY FROM 2.5 TO 90 VOLTS DEPENDING ON MANUFACTURES.
- A METER NEEDLE WILL OSCILLATE





.40 9 .50 10 1,02 11 3,90 12 3,90 13 .1 14 .52 13 5,0 16	IC12 IC13 IC14 IC16 IC16	8 0 7 .14 6 .15 5 5.74 4 5.74 3 .17 2 .16 1 .48
.11 9 .11 10 3.82 11 0 12 .34 13 5.84 14 .11 15 .12 [8	IC18 IC19 IC20 IC21 IC22	8 5.30 2 .15 0 .14 5 5.0 4 .34 . 3 .16 2 .17 1 5.85
33 • 1 42 • 2 .13 3 .11 4 5.0 50	IC26 IC27 IC20 IC29 IC30	16.150 15 0 39 14 0 21 13 0 33

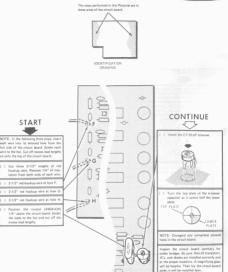
**@** 25

**@** 38

410 8

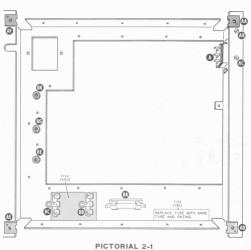
# PARTS PICTOR (A7 (A6) (A5) 14 (A10) NOTE: HEATH PART NUMBERS ARE STAMPED ON MOST DIODES. (A9) (B3) B2 (6) (C3 (B) (C)

CTORIAL

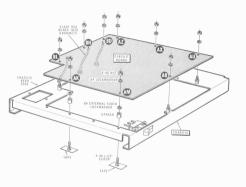


START

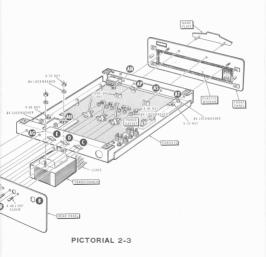
FINISH

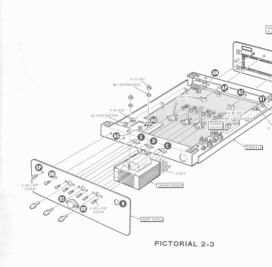


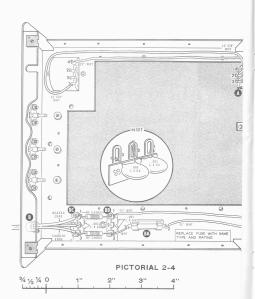
FIGTORIAL 2-1



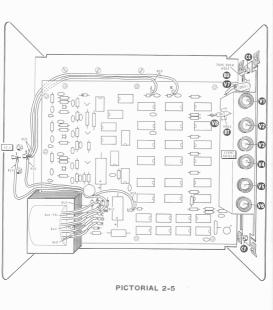
PICTORIAL 2-2

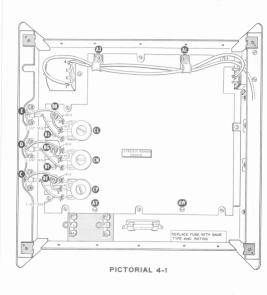


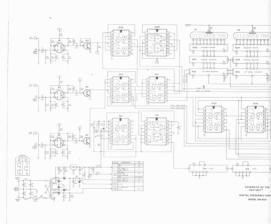


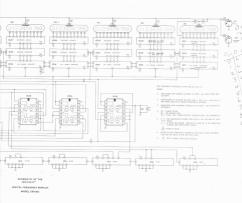












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- A brief description of the difficulty.
- Authorization to return your kit C.O.D. for the service and shipping charges. (This will reduce the possibility of delay.)

Once, the equipment to see that all acrees and parts are ascered. (De, not include any vooders cabletes or color ascered, to the color and the

Seel the carton with reinforced gummed tape, tie it with a strong cord, and mark it "Fragile" on at least two sides. Remember, the carrier will not accept liability for shipping dumage if the unit is insufficiently packed. Ship by prepaid express, United Parcel Service, or insured Parcel Post to:

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